PBL#1 Report: Counter (0-9)

Name:

ID:

# Introduction

Please introduce your design project.

# Verilog HDL Design Code

Please write you design code in this section.

# Simulation Waveform

Please screen shot your simulation waveform in this section. Notice that all the test functions must be shown in your waveform.

# Comments

Please comment your design projects, including challenges, constraints, conclusions, future works, etc.